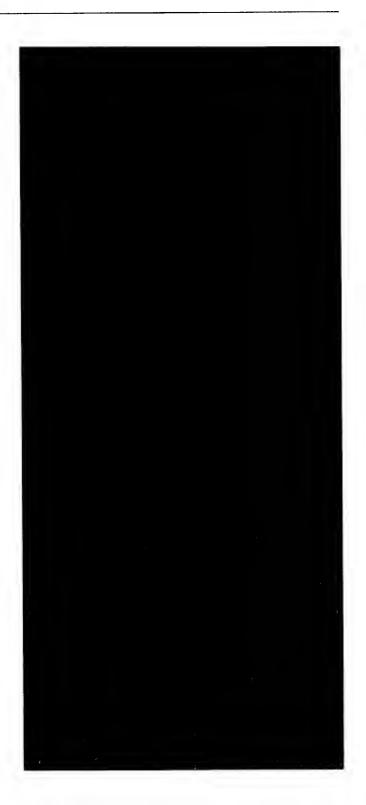
Honeywell

O16-XREF, SSUP, AND MAC SOURCE LANGUAGE PROCESSORS

S	E	R	ES	16

SOFTWARE



Honeywell

O16-XREF, SSUP, AND MAC SOURCE LANGUAGE PROCESSORS

SERIES 16

SUBJECT:

Description, System Generation, Operation, Memory Requirements, and Programming Examples of Three Source Language Processors.

DATE:

July 1971

ORDER NUMBER:

AC94, Rev. 0 (Formerly M-459)

DOCUMENT NUMBER:

70130072582A

PREFACE

This manual provides the programmers and operators of Series 16 computer systems with the information necessary to use the following Honeywell-supplied source language processors:

O16-XREF

Cross-Reference Listing

SSUP

Symbolic Source Update

MAC

Macro Instruction Processor

Elements of the languages, examples of their use, exception conditions, and related supporting programs are described. Instructions for generating stand-alone systems and instructions for executing the programs are also included.

The reader is assumed to have a basic familiarity with Series 16 assembly language programming and to have read the 316/516 Programmers' Reference Manual, Order Number BX47 (formerly M-490).

The illustration on page v provides an overview of the relationship among the three processors and other elements of the assembly language system.

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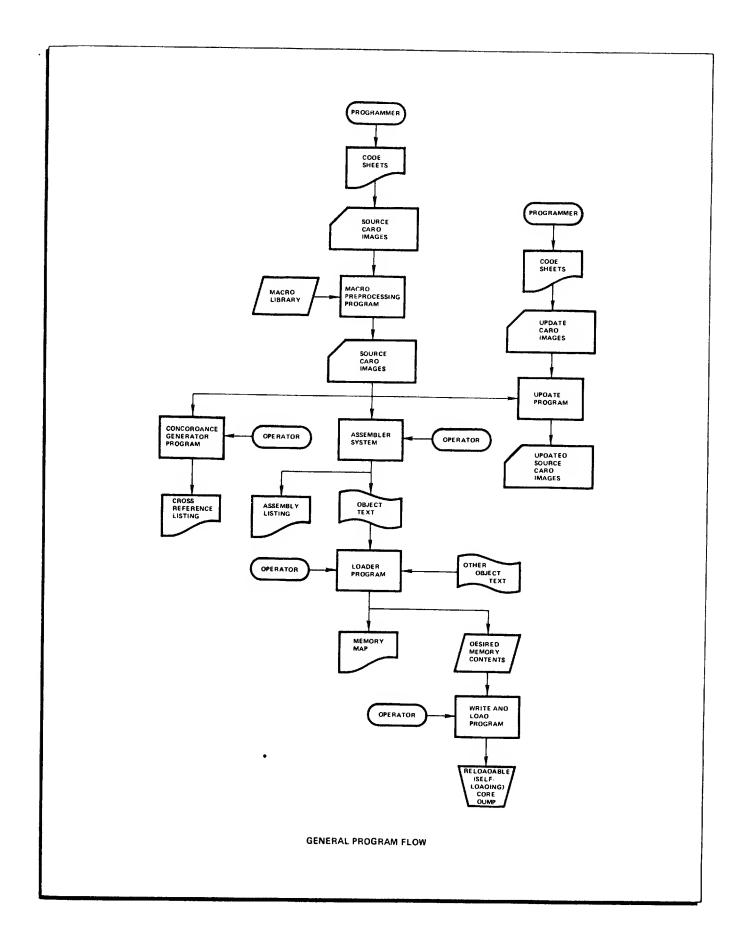
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SECTION I

MAC MACRO PROCESSOR FOR DAP-16

The Macro Processor (MAC) is a single-pass processor used to expand DAP-16 Assembly Language sources containing prototype macro definitions and statements. Macro definitions can be entered from a separate library or they can be embedded in a single source. The definitions are retained in main memory for the duration of the job. As the macro prototypes are expanded according to a macro statement, the actual arguments of the macro statement replace the dummy arguments of the macro definition. The MAC output is assembly source code which is suitable for input to the DAP-16 Mod 2 Assembler.

GENERAL DESCRIPTION

The macro language provides a convenient way to generate a desired sequence of assembly language statements many times in one or more programs. The macro definition which defines the desired sequence of statements is written once near the top of the program. Then, a single statement, the macro statement, is written each time the desired code is to be generated. This simplifies program coding, reduces the chance of programming error, and ensures that standard sequences of statements are used to accomplish desired functions.

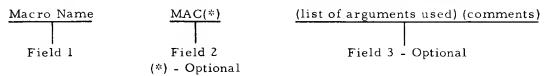
Conditional assembly can also be used with the MAC processor. In this way, statements can be coded which may or may not be assembled, depending upon conditions evaluated at assembly time. These conditions are usually tests of values, which can be defined, set, or changed during a macro expansion.

Macro Definition

Every macro definition is divided into three major components: the header (or first) statement, the body of the macro which specifies the prototype, and the end (or last) statement.

HEADER STATEMENT

The first record of every macro definition must conform to the following:



Field	Columns	Contents
1	l through 4	Macro name, maximum of four characters (cannot have * or \$ as first character).
2	6 through 8	The letters MAC.
	9	An asterisk if the macro expansion is to be preceded by two comment records. A space if the comment records are to be omitted.
3	12 through 72	An optional argument list, comments, and/or identification statements which are ignored by the processor.

MACRO DEFINITION BODY

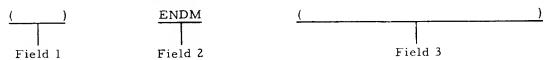
The body of the macro definition (DAP-16 format assumed) consists of prototype code containing dummy variables. The dummy variables are replaced by the arguments passed in the macro statement when the statement is expanded. The expanded source code is output in the same relative position from which the macro instruction statement was read. Comment lines within the definition will be output unchanged when the macro prototype is expanded. Any occurrence of the character pair #0 will be replaced by a single digit between one and nine which represents the number of actual arguments in the macro statement (call). This digit can be less than, equal to, or greater than the number of dummy arguments, but in no case can it exceed nine.

Any occurrence of the character pairs #1 through #9 will be replaced by a copy of the corresponding actual argument when the macro is expanded. This point is illustrated in the examples which follow.

Macro statements referencing previously defined macros are allowed in the body of a macro definition. Macro names included in the argument list require additional passes to complete expansion (i.e., the MAC processor is not re-entrant). Likewise, recursive use requires an additional number of passes equal to the degree of recursiveness.

END STATEMENT

The last record of every macro definition must contain the following:



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Field	Columns	Contents
1	l through 5	Ignored.
2	6 through 9	The letters ENDM.
3	10 through 120	Ignored.

Macro Statement

A macro statement can call a macro any number of times after the macro has been defined by the macro definition. Each macro statement must contain the following:

(label)	Macro Name	(arg 1, arg 2arg 9) (comments)
 Field l	Field 2	 Field 3
Field	Columns	Contents
1	l through 4	Label or blank.
	5	lgnored.
2	6 through 9	Name of macro being called.
	10 and 11	lgnored.
3	12 through 72	The argument list terminated by one or more blanks. Each argument may consist of any number of characters. Any character may be used except a comma or a blank. A pound sign (#, '243) is converted into a blank during the expansion. Arguments are separated by commas. Null arguments are acceptable.
	73 through 120	lgnored.

Data Format

Source data for MAC is prepared in a manner similar to the source data for DAP-16. If the standard input routines are used, tabs will be set at columns 6, 12, 30, and 73. The backslash character can be used to compress the source tape. If MAC output is punched on paper tape using the standard output routines, backslash characters are used to tab to columns 6, 12, 30, and 73.

End-of-text records (ETX, '003 or '203 on the ASR or high-speed reader, or 11-8-6 for cards) are ignored on input and are not generated on output.

Input will be halted during a job upon detection of a MOR pseudo-operation. This feature allows the concatenation of macro libraries and other sources. It also allows the changing of input device selection.

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One file per job is processed by the MAC processor. If the source is entered from paper tape or cards, the file must be terminated by a record containing a dollar sign (\$, '044 or '244) in column 1. The letters END may follow in columns 2 through 4. On magnetic tape, files may also be terminated by a tape mark.

Magnetic tape input allows records of up to 120 characters. This facilitates the processing of source material with the Honeywell Series 16 FORTRAN system, which writes 120-character formatted records.

Magnetic tape output is written in 120-character records. Thus, subsequent language processors must accept this record length.

Errors

MAC recognizes five error conditions. For each of these, a message is printed on the teletypewriter in the following form:

* (error flag) record being processed

The error flags are listed in Table 1-1.

Table 1-1. MAC Error Messages

Error Flag	Description	
А	More than nine arguments in the actual argument list. The request for the number of arguments (#0) is not counted.	
F	Field overflow in macro expansion. An attempt has been made to store a character other than a space in or beyond columns 5, 11, 29, or 72. MAC ignores this error.	
М	MAC pseudo-operation in macro definition. This record is ignored.	
Ο	Memory overflow. There is insufficient space in the free core area to store all macro definitions. This error ends the job.	
P	Formal argument not #0 through #9. A # which is not followed by a single decimal digit has been detected within a macro body. This error is not detected until the macro is expanded. MAC goes to the next field of the current record of the macro body.	

SYSTEM GENERATION

The generation of a MAC system is done in a straightforward fashion. The relocatable object of MAC is loaded, usually at the default starting address of '1000. It will call the relocatable input/output supervisor, MAC-IOS, which in turn will call the off-line I/O

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driver programs. The drivers for the desired devices, including the magnetic tape support package, may be loaded in any order. Finally the calls for unwanted devices must be satisfied with G\$DR, which will also pass to MAC-IOS the bounds of available memory.

If G\$DR is loaded in the extended desectorizing mode (EXD), MAC will be entered in the extended mode (EXA) and memory through 32K will be used. If G\$DR is loaded in the normal desectorizing mode (LXD), MAC will be entered in the normal mode (DXA) and memory through 16K will be used regardless of the presence of additional memory.

The keyword is loaded at G\$DR, which may be located by obtaining a core map. As loaded, the keyword contains '000022, which selects paper tape input (with parity) and output. The keyword default value may be changed at this time.

OPERATION (Keyword)

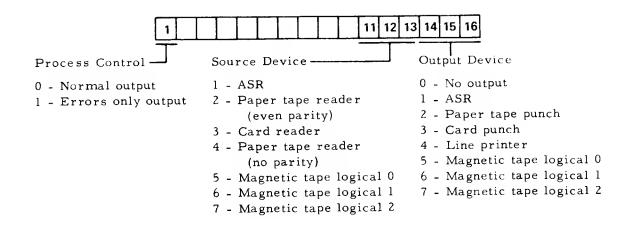
Prior to starting MAC, the source to be expanded, which must have all macro prototypes physically before they are required, must be ready, as must the output device. Set the keyword in the A register if other than the default value is used and start at the first address of MAC, normally '1000.

The source will be read until either a MOR pseudo-operation is encountered or a MAC end-of-file is detected. The latter must be either a dollar sign in column 1 or a tape mark. Any ETX records on paper tape or cards are ignored. The MOR pseudo-operation will cause the message MORE to be typed and the processor will stop for operator action. The keyword, which may be changed before restart, will be displayed in the A register.

Output may be generated immediately depending upon the input data. If paper tape is being used, MAC will punch leader and trailer.

The A register must be set with the keyword at the start of the program. If the word is zero, the default word ('000022 unless changed at system generation) will be used. If any bit is set, the A register must contain the entire desired keyword. The A register setting for the keyword is shown on the following page.

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MESSAGES

The only non-error run message is MORE, which indicates that a MOR pseudooperation was encountered.

A MAC error message may be typed as indicated in "Errors" above. Other error messages are:

DEVICE NOT AVAILABLE
RECORD UNREADABLE (magnetic tape only)
END OF TAPE (input or output, magnetic tape only)

The end of job is indicated by:

** MACRO ERRORS

or NO MACRO ERRORS

PAPER TAPE PARITY

MAC includes support of paper tape punched with even parity. Honeywell does not supply Teletypes equipped to punch parity in the United States, but the option is common elsewhere. An error is processed within the driver program and the appropriate documentation must be consulted for advice. All output on the paper tape punch is made with level 8 marking (i.e., punched).

TERMINATION

At the end of the job, magnetic tape input is positioned after the tape mark or the dollar sign record. An output tape will have a tape mark but not an \$END record. The output tape is left just beyond the tape mark.

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Other output devices will be terminated with a record containing \$END columns 1 through 4.

The A register will contain the keyword. A new job may be started from the halting location.

INTERNAL MAC EXPANSION PROCESSING

The input stream is scanned for macro instruction statements one record at a time. Upon detecting a macro instruction statement, two comment records are output, if the prototype indicates the selection (an asterisk in column 9 of the first prototype statement). The first optional comment record contains an asterisk in column 1, followed by blanks. The second contains an asterisk in column 1, blanks in columns 2 through 4, and a copy of the macro statement in column 5 through 120.

Following the comment records, the prototype is expanded. Each record of the macro prototype is examined and comment records within the macro definition are output unchanged. Other records are expanded field by field (i.e., columns 1 through 5, 6 through 11, 12 through 29, and 30 through 72). Any occurrence of the character pair #0 is replaced by a single digit between 1 and 9 that represents the number of actual arguments found in the input record. The character pairs #1 through #9 are replaced by their respective actual arguments listed in the macro statement (i.e., #1 is replaced by the first actual argument, #2 is replaced by the second actual argument, etc.). If the actual argument does not exist, the whole record is ignored and MAC goes to the next record of the macro prototype. All pound signs in the arguments are converted to spaces in the output record, except in the two optional comment records preceding a macro expansion.

Any noncomment records which have an operation field (columns 6 through 9) previously defined by a stored macro definition are expanded. All other records are copied unchanged.

MEMORY REQUIREMENTS

The macro prototypes are packed into core. The MAC and ENDM records require a total of seven half-words. For other records in the macro prototypes, one half-word is required for each nonspace character, one-half word is required for each sequence of spaces not at the end of a record, and one-half word additional is required for each record.

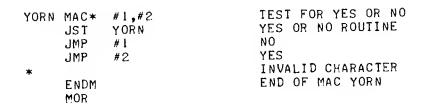
MACRO EXAMPLES

Five examples of macro applications are presented in this section. If the MAC output

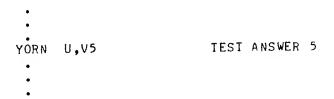
is punched on paper tape, it contains backslash characters to represent tab to columns 6, 12, 30, and 73. The MAC output listings in this manual have been expanded to the correct tab position for each of reading.

Generation of Calling Sequences Using Subroutines

If a subroutine is called many times during a long program, a macro definition of the calling sequence can be written. A typical macro definition for a call to subroutine YORN is:



After the macro has been defined, a macro statement which references the macro definition can be inserted at any point in the coding:



MAC expands the prototype which generates the coding shown below. The first two lines are the optional comment lines, which have been generated because the first statement of the macro definition contains an asterisk in column 9.

```
*

* YORN U,V5

JST YORN

JMP U

JMP V5

*

INVALID CHARACTER
```

Use of #0 in Macro Expansion

Any occurrence of the character pair #0 will be replaced by a single digit between 0 and 9 when the macro is expanded. The number which replaces the character pair #0 represents the number of actual arguments passed from the macro statement. It can be less

than, equal to, or greater than the number of formal arguments but in no case can it exceed 9.

In the following example, the prototype code for the previous example is modified by adding the character pair #0 in three places:

The same macro statement is used to cause the expansion shown below:

*			
*	YORN	U, V5	TEST ANSWER 5
	JST	YORN	YES OR NO ROUTINE
	JMP	ប	NO,2
	JMP	V5	YES
*			INVALID CHARACTER, O

Note that the first occurrence of the character pair #0 was in the dummy argument list and does not appear in the expansion. The second occurrence of #0 is in a coding $\lim_{\epsilon \to 0} \frac{1}{\epsilon}$ and is replaced by 2, which is the number of actual arguments. The last occurrence of #0 is in a comment line. In this case, the pound sign is taken as a space and the character 0 is printed unchanged.

Generation of Complete In-Line Coding

This type of coding may be inefficient in terms of memory used, but it is simpler than calling a subroutine and runs faster. An example containing two macro definitions is presented in Figure 1-1. The macro definitions are placed at the beginning of the program. The macro statements which reference the macro definitions are placed within the body of the program as frequently as desired.

MAC generates the coding presented in Figure 1-2 each time the prototypes are expanded.

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```
FROM, WITH, COUNT
FILL MAC*
           #1,#2,#3
                              #3 WORDS
     LDX
           = -# 3
                              FILL WITH #2
     LDA
           #2
                              STARTING WITH #1
     STA
           #1+#3,1
                              TALLY INDEX
     IRS
           0
     JMP
           *-2
                              REPEAT TO END OF BUFFER
                              END OF MAC FILL
     ENDM
COPY MAC*
                             FROM, TO, COUNT
           #1,#2,#3
                              #3 WOPDS
     LDX
           =-#3
                              COPY FROM #1
     LDA
           #1+#3,1
                              TO #2
           #2+#3,1
     STA
     IRS
                              TALLY INDEX
           0
                              REPEAT TO END OF BUFFERS
     JMP
           *-3
                              END OF MAC COPY
     ENDM
                    Macro Definitions
                              COPY TO OUTPUT RUFFER
     COPY
           IN, OUT, 36
                              CLEAR INPUT BUF 'ER
           IN,=A##,60
     FILL
                  Macro In-Line Coding
```

Figure 1-1. In-Line Coding Example - Source Input

```
COPY TO OUTPUT RUFFER
COPY
      IN,OUT,36
                        36 WORDS
LDX
      =-36
      10+36.1
                        COPY FROM IN
LDA
      OUT+36,1
                        TO OUT
STA
                        TALLY INDEX
IRS
                        REPEAT TO END OF BUFFERS
JMP
      *-3
                        CLEAR INPUT BUFFER
FILL
      IN,=A##,60
                         60 WORDS
LDX
      =-60
                         FILL WITH =A
LDA
      =A
                         STARTING WITH IN
      IN+60,1
STA
                        TALLY INDEX
IRS
      0
                         REPEAT TO END OF BUFFER
      *-2
JMP
```

Figure 1-2. MAC Expansion of In-Line Coding

Using Macros to Implement Interpretive Scheme

In this example, a scheme is presented which will allow the coding of a process of several steps involving complex variables. The macro definitions used in this example are presented in Figure 1-3.

With these macro definitions, it is possible to code the complex equation Y = A * SIN (A * T1 + B * T2) * COS (B * T1-A *2) as the series of macro statements shown in Figure 1-4.

The assembled output of MAC will be similar to the listing presented in Figure 1-5.

1-11 #AC94

	MAC I	LOAD COMPLEX VARIABLE
	CALL L\$55 DAC #1	LOAD COMPLEX VARIABLE
	ENDM	END OF MAC BLANK
	MAC 1	COMPLEX ADD
	CALL A\$55 DAC #1	CONFLEX ADD
	ENDM	END OF MAC +
	MAC I CALL S\$55	COMPLEX SUBTRACT
	DAC #1	COM LEX SOCIANO!
	ENDM	END OF MAC -
	MAC #1 CALL MS55	COMPLEX MULTIPLY
	DAC #1	
	ENDM	END OF MAC X*
	MAC #1 CALL DS55	COMPLEX DIVIDE
	DAC #1	
	ENDM	END OF MAC /
NEG	MAC CALL N\$55	COMPLEX NEGATE
	DAC #1	500 OF MAG 1950
	ENDM MAC I	END OF MAC "EG
=	MAC I CALL H\$55	STORE COMPLEX VARIABLE
	DAC #1	END OF MAC -
SIN	ENDM MAC	END OF MAC =
51"	= W	
	CALL CSIN	COMPLEX SINE
	DAC W ENDM	END OF MAC SIN
Cos	MAC	
	E W CALL CCOS	COMPLEX COSINE
	DAC W	
	ENDM	END OF MAC COS
EXP	MAC W	
	CALL CEXP	COMPLEX EXPONENTIAL
	DAC W ENDM	END OF MAC EXP
LN	MAC	END OF ORD WITH
	= W	COMPLEY LOC
	CALL CLOG	COMPLEX LOG
1	ENDM	END OF MAC LN
SQRT		
	= W CALL CSQR	T COMPLEX SQUARE ROOT
	DAC W	
450	ENDM	END OF MAC SQRT
ABS	MAC = W	
	CALL CABS	COMPLEX ABSOLUTE
	DAC W ENDM	END OF MAC ABS
	MOR	

Figure 1-3. Macro Definitions for Interpretive Scheme

1-12 #AC94

```
LOAD A
      X*
              TI
                                       A*T1
                                       STORE A*TI
      =
              W+ 4
              В
                                       LOAD B
      X*
              ST
                                       B*T2
              W+4
                                       A*T1+B*T2
      SIN
                                      SINE OF (A)
SINE OF (A)*A
STORE SINE OF (A)*A
      X*
              ₩+4
              Α
                                       LOAD A
              T2
                                       A*T2
      χ*
                                       STORE A*T2
              W+8
              В
                                       LOAD B
      χ*
              Tl
                                       B*T1
              ₩+8
                                       B*T1-A*T2
                                      COSINE OF (A)
A*SIN*COS
STORE Y
      COS
              W+4
      χ*
              Y
      BSZ
              8
Y
      BSZ
              4
      END
```

Figure 1-4. Macro Statements for Interpretive Scheme

1-13 #AC94

0034 00041 0 000050 DAC W 0035 00042 0 10 00000 CALL CCOS COMPLEX COSINE 0036 00043 0 000050 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC W+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE				
0003 00002 0				LOAD COMPLEX VARIABLE
0004 00003 0 000064 DAC T1	0002 00001	0 000067		
O005				COMPLEX MULTIPLY
0006 00005 0 000054 DAC W*4 0007 00006 0 10 00000 CALL L\$55 LOAD COMPLEX VARIABLE 0009 00010 0 10 00000 DAC T2 0011 00012 0 10 00000 CALL A\$55 COMPLEX ADD 0012 00013 0 000054 DAC W*4 0013 00014 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0014 00015 0 000050 DAC W 0016 00017 0 000050 DAC W 0017 000020 0 10 00000 CALL H\$55 COMPLEX ADD 0116 00017 0 000050 DAC W 0017 000020 0 10 00000 CALL H\$55 COMPLEX VARIABLE 0020 00023 0 000054 DAC W*4 0020 00023 0 000054 DAC W*4 0020 00023 0 000054 DAC W*4 0021 00024 0 10 00000 CALL L\$55 COMPLEX VARIABLE 0022 00025 0 000066 DAC W*4 0023 00026 0 10 00000 CALL H\$55 COMPLEX VARIABLE 0024 00027 0 000066 DAC T2 0025 00030 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0026 00031 0 000066 DAC T2 0027 00032 0 10 00000 CALL H\$55 COMPLEX VARIABLE 0026 00031 0 000066 DAC T2 0027 00032 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0028 00030 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0029 00034 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0029 00034 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0029 00034 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0029 00034 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0030 00035 0 000066 DAC W*8 0030 00035 0 000066 DAC W*8 0030 00035 0 000066 DAC W*8 0031 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0032 00037 0 000060 DAC W*8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC W*8 0035 00042 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0036 00043 0 000050 DAC W*8 0037 00044 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0036 00045 0 000050 DAC W*8 0037 00044 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0038 00045 0 000050 DAC W*8 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE		• ,		
0007				STORE COMPLEX VARIABLE
0008 00007 0 000065 DAC B 0009 00010 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0010 00011 0 000066 DAC T2 0011 00012 0 10 00000 CALL A\$55 COMPLEX ADD 0012 00013 0 000054 DAC M 0013 00014 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0014 00015 0 000050 DAC M 0015 00016 0 10 00000 CALL CSIN COMPLEX SINE 0016 00017 0 000050 DAC M 0017 00020 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0018 00021 0 000067 DAC A 0019 00022 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0020 00023 0 000054 DAC M 0021 00024 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0020 00025 0 000067 DAC A 0021 00024 0 10 00000 CALL L\$55 COMPLEX VARIABLE 0022 00025 0 000067 DAC A 0023 00026 0 10 00000 CALL L\$55 COMPLEX WALTIPLY 0024 00027 0 000066 DAC M\$55 STORE COMPLEX VARIABLE 0025 00030 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0026 00031 0 000066 DAC T2 0027 00032 0 10 00000 CALL L\$55 COMPLEX WALTIPLY 0028 07033 0 10 00000 CALL L\$55 COMPLEX WALTIPLY 0030 00035 0 000066 DAC B 0029 00034 0 10 00000 CALL L\$55 COMPLEX WALTIPLY 0030 00035 0 000066 DAC B 0029 00034 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0029 00034 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0030 00035 0 000064 DAC B 0030 00036 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0031 00036 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0033 00036 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC M 0035 00044 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0036 00043 0 000050 DAC M 0037 00044 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0038 00045 0 000054 DAC M 0039 00046 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE	• •	• .		
0009				LOAD COMPLEX VARIABLE
0010 00011 0 000066				
10011 00012				CUMPLEX MULITPLY
0012 00013 0 000054 DAC H+4 0013 00014 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0014 00015 0 000050 DAC H 0015 00016 0 10 00000 CALL CSIN COMPLEX SINE 0016 00017 0 000050 DAC H 0017 00020 0 10 00000 CALL H\$55 STORE COMPLEX MULTIPLY 0018 00021 0 000067 DAC A 0019 00022 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0020 00023 0 000054 DAC H-4 0021 00024 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0022 00075 0 000067 DAC A 0023 00026 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0024 00027 0 000066 DAC T2 0025 00030 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0026 00031 0 000060 DAC H+8 0027 00032 0 10 00000 CALL L\$55 STORE COMPLEX VARIABLE 0028 09033 0 000065 DAC H-8 0029 00034 0 10 00000 CALL L\$55 COMPLEX VARIABLE 0028 09033 0 000065 DAC H 0030 00035 0 000064 DAC T1 0030 00035 0 000064 DAC T1 0030 00035 0 000064 DAC T1 0031 00036 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0033 00040 0 10 00000 CALL H\$55 COMPLEX SUBTRACT 0032 00037 0 000060 DAC H+8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0035 00042 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0036 00043 0 000050 DAC H 0037 00044 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC H 0039 00046 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0039 00046 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0039 00046 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0039 00046 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0039 00046 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0039 00046 0 10 00000 CALL H\$55 COMPLEX MULTIPLY 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0039 00047 0 000060 DAC H+8	-	~ ~		COURT FW ARR
0013 00014		•		CUMPLEX ADD
0014			-	CTOBE COMPLEY WAR (ADIE
0015 00016	• • • • • • • • • • • • • • • • • • • •		- -	STURE GUMPLEA VARIABLE
0016 00017 0 000050 DAC W 0017 00020 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0018 00021 0 000067 DAC A 0019 00022 0 10 000000 CALL H\$55 STORE COMPLEX VARIABLE 0020 00023 0 000054 DAC W+4 0021 00024 0 10 00000 CALL L\$55 LOAD COMPLEX VARIABLE 0022 00025 0 000067 DAC A 0023 00026 0 10 00000 CALL H\$55 SORE COMPLEX VARIABLE 0024 00027 0 000066 DAC T2 0025 00030 0 10 00000 CALL H\$55 SORE COMPLEX VARIABLE 0026 00031 0 000060 DAC W+8 0027 00032 0 10 00000 CALL L\$55 LCAD COMPLEX VARIABLE 0028 070033 0 000065 DAC B 0029 00034 0 10 00000 CALL L\$55 COMPLEX VARIABLE 0029 00034 0 10 00000 CALL L\$55 COMPLEX VARIABLE 0030 00035 0 000064 DAC H\$55 COMPLEX WULTIPLY 0030 00035 0 000064 DAC F1 0031 00036 0 10 00000 CALL S\$55 COMPLEX SUBTRACT 0032 00037 0 000060 DAC W+8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC W 0035 00042 0 10 00000 CALL H\$55 COMPLEX VARIABLE 0036 00043 0 000050 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX VARIABLE 0037 00044 0 10 00000 CALL M\$55 COMPLEX VARIABLE 0038 00045 0 000054 DAC W 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0040 00047 0 000060 DAC W 0040 00047 0 000060 DAC W 0040 00047 0 000000 CALL H\$55 STORE COMPLEX VARIABLE		-		COMM. LY SINE
0017 00020			-	COMPLEX SINE
0018 00021		• • • • • • •	·	CAMOLEY MILL TIPLY
0019 00022 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0020 00023 0 000054 DAC H+4			_	Confeek notifier
0020 00023 0 000054 DAC W+4 0021 00024 0 10 00000 CALL L\$55 LOAD COMPLEX VARIABLE 0022 00025 0 000067 DAC A 0023 00026 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0024 00027 0 000066 DAC T2 0025 00030 0 10 00000 CALL H\$55 SORE COMPLEX VARIABLE 0026 00031 0 000060 DAC W+8 0027 00032 0 10 00000 CALL L\$55 L(AD COMPLEX VARIABLE 0028 09033 0 000065 DAC B 0029 00034 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0030 00035 0 000064 DAC I1 0031 00036 0 10 00000 CALL B\$55 COMPLEX MULTIPLY 0032 00037 0 000060 DAC W+8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC W 0035 00042 0 10 00000 CALL CCOS COMPLEX VARIABLE 0036 00043 0 000050 DAC W 0037 00044 0 10 00000 CALL CCOS COMPLEX MULTIPLY 0038 00045 0 000054 DAC W 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX MULTIPLY 0038 00045 0 000054 DAC W 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE		• • • • • • • • • • • • • • • • • • • •	=•	STORE COMPLEY VARIABLE
0021 00024				STORE COURTER ANTINOE
0022 00025 0 000067 DAC A 0023 00026 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0024 00027 0 000066 DAC T2 0025 00030 0 10 00000 CALL H\$55 S ORE COMPLEX VARIABLE 0026 00031 0 000060 DAC W+8 0027 00032 0 10 00000 CALL S\$5 L(AD COMPLEX VARIABLE 0028 07033 0 000065 DAC B 0029 00034 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0030 00035 0 000064 DAC F1 0031 00036 0 10 00000 CALL S\$55 COMPLEX SUBTRACT 0032 00037 0 000060 DAC W+8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC W 0035 00042 0 10 00000 CALL COS COMPLEX COSINE 0036 00043 0 00050 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC W 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX MULTIPLY 0038 00047 0 000060 DAC W+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0040 00047 0 000060 DAC Y			- · · · · · · · · · · · · · · · · · · ·	LOAD COMPLEY VARIABLE
0023 00026				TONE COM TEX TANTABLE
0024 00027 0 000066 DAC T2 0025 00030 0 10 00000 CALL H\$55 S ORE COMPLEX VARIABLE 0026 00031 0 000060 DAC H+8 0027 00032 0 10 00000 CALL L\$55 L(AD COMPLEX VARIABLE 0028 00033 0 000065 DAC B 0029 00034 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0030 00035 0 000064 DAC F1 0031 00036 0 10 00000 CALL S\$55 COMPLEX SUBTRACT 0032 00037 0 000060 DAC H+8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC H 0035 00042 0 10 00000 CALL CCOS COMPLEX COSINE 0036 00043 0 000050 DAC H 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC H+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0040 00047 0 000060 DAC Y				COMPLEY MULTIPLY
0025 00030			- · · · · · · · · · · · · · · · · · · ·	Governous Liver
0026 00031 0 000060 DAC W+8 0027 00032 0 10 00000 CALL L\$55 L(AD COMPLEX VARIABLE 0028 07033 0 000065 DAC B 0029 00034 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0030 00035 0 000064 DAC F1 0031 00036 0 10 00000 CALL S\$55 COMPLEX SUBTRACT 0032 00037 0 000060 DAC W+8 0033 00040 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0034 00041 0 000050 DAC W 0035 00042 0 10 00000 CALL CCOS COMPLEX COSINE 0036 00043 0 000050 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC W+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0040 00047 0 000060 DAC Y			= · · = ·	S ORE COMPLEX VARIABLE
0027 00632 0 10 00000				J dite do
0028 09033				LIAD COMPLEX VARIABLE
0029 00034				
0030 00035				COMPLEX MULTIPLY
0031 00036				
0032 00037		• • • • •		COMPLEX SUBTRACT
0033 00040			DAC W+8	
0034 00041 0 000050 DAC W 0035 00042 0 10 00000 CALL CCOS COMPLEX COSINE 0036 00043 0 000050 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC W+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE			CALL H\$55	STORE COMPLEX VARIABLE
0035 00042		0 000050	DAC W	
0036 00043 0 000050 DAC W 0037 00044 0 10 00000 CALL M\$55 COMPLEX MULTIPLY 0038 00045 0 000054 DAC W+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0040 00047 0 000060 DAC Y			CALL CCOS	COMPLEX COSINE
0037 00044		0 000050	DAC W	
0038 00045 0 000054 DAC W+4 0039 00046 0 10 00000 CALL H\$55 STORE COMPLEX VARIABLE 0040 00047 0 000060 DAC Y		0 10 00000	CALL MS55	COMPLEX MULTIPLY
0039 00046		0 000054	DAC W+4	
00.0000		0 10 00000	CALL H\$55	STORE COMPLEX VARIABLE
I was a second of the BOT O	0040 00047	0 000060	DAC Y	
0012 00000 00000	0041 00050	000000 W	BSZ 8	
0042 00060 000000 Y BSZ 4	0042 00080	000000 Y	BSZ 4	
0043 END	0043		END	

Figure 1-5. Assembler Listing for Interpretive Scheme

Macros Used With Conditional Assembly

Macro prototypes can be coded to include conditional assembly statements and the assembly process thereby directed by the value (or presence) of certain arguments. Coding using conditional pseudo-operations to call either the ASR or line printer off-line driver program is shown in Figure 1-6.

```
PRNT MAC*
           #1,#2
                               TEST FOR OUTPUT DEVICE
     IFZ
           #1-4
                               ZERO IF LINE PRINTER
                               CALL L.P.
     CALL
            O$LA
     DAC
            #2
                               BUFFER ADDRESS
                               END OF L.P. CONDITIONAL
     ENDC
     IFZ
           #1-1
                               ZERO IF ASR
     CALL
           0$LL
                               CALL ASR
     DAC
           #2
                               BUFFER ADDRESS
     ENDC
                               END OF ASR CONDITIONAL
     IFN
           #1-1
                               ASR TEST
     IFN
           #1-4
                               L.P. TEST
     FAIL
                               ERROR CONDITION
                               END OF L.P. TEST
     E NDC
                               END OF ASR TEST END OF MAC PRNT
     ENDC
     ENDM
     MOR
```

Figure 1-6. Macro Definition Using Conditional Pseudo-Operations

Following the macro definition, the line printer can be called using the following macro statement:

```
PRNT 4,DATA I=ASR,4=LINE PRINTER
```

When the MAC output is assembled, the program listing will contain the line printer call:

```
* PPNT 4.DATA 1=ASR.4=LINE PRINTER
CALL OSLA CALL 1.P.
DAG DATA RUFFER ADDRESS
```

Conversely, to call the ASR:

```
PRNT 1,DATA 1=ASR,4=LINE PRINTER
...
```

results in:

```
PRNT 1.DATA

CALL OFLL

DAC DATA

1=ASR.4=LINE PRINTER
CALL ASR
BHFFFR ADDRESS
```

If neither 1 nor 4 is passed as an argument, the FAIL pseudo-operation will be assembled and an error flagged:

```
* PRNT 3,NODT 1=ASR,4=LINE PRINTER FAIL ERROR CONDITION
```

Trace Example Using Macros and Conditional Assembly

In this example, a macro definition to call FORTRAN IV Trace Program F\$TR from an assembly language program is shown. Conditional assembly pseudo-operations are used to distinguish between a statement number (label) trace and other traces, and the call is adjusted accordingly. The macro definition used in this examp e is shown as Figure 1-7.

```
TYPE.NAME
TR
     MAC
                                O=TRACE STATEMENT NO.
     IFZ
            #1
           F$ TR
     CALL
            3,#1,5,'37,8,'240 '37=HALF<,'240 PREVENTS AT SIGN
     VFD
     BCI
     ELSE
            #2
     IMA
     CALL
            F$ TR
            3,#1
     VFD
            2,#2
     BCI
            #2
     IMA
     ENDC
     ENDM
      MOR
```

Figure 1-7. Macro Definition for Conditional Trace Example

In the example, it was decided to trace the logical variable FLAG, the interger variable A, and the label TWO. F\$TR requires the argument #1 to be 3, 1, and 0, respectively, to identify these types of variables. The macro statement TR and the variable type and name arguments were then inserted into the existing code at the points where tracing was desired. The results are shown in Figure 1-8.

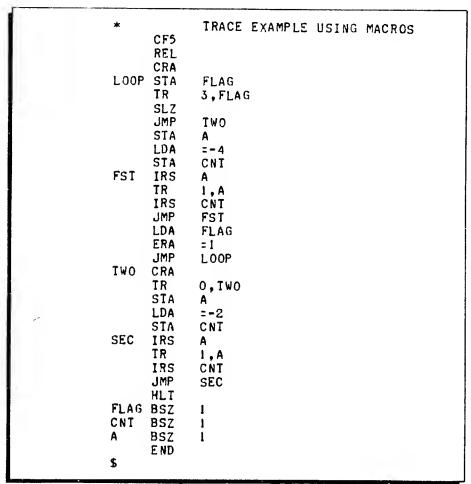


Figure 1-8. Trace Example Program

Each macro statement within the coding is expanded and the actual arguments are substituted in the MAC output coding. A portion of this coding is presented in Figure 1-9.

When the MAC output is assembled, the conditional pseudo-operations cause the inappropriate lines to be ignored. The assembly listing is presented in Figure 1-10.

Following the assembly, the program was loaded and executed. The trace shown below was printed on the Teletype. The @ signs result from some assumptions within F\$TR relative to the (normally) compiler-generated call.

```
    2 FLAG= F

    6A= 1

    6A= 2

    6A= 3

    6A= 4

    6 FLAG= T

    (TWO)

    6A= 1

    6A= 2
```

```
TRACE EXAMPLE USING MACROS
     CF5
     REL
     CRA
LOOP STA
            FLAG
                               O=TRACE STATEMENT NO.
     IFZ
           F$ TR
     CALL
           3,3,5,'37,8,'240 '37=HALF<,'240 PREVENTS AT SIGN
     VFD
            2,FLAG
     BCI
     ELSE
     IMA
            FLAG
            F$ TR
     CALL
     VFD
            3,3
            2,FLAG
     BCI
            FLAG
     IMA
     ENDC
     SLZ
      JMP
            TWO
      STA
            Α
            = -4
      LDA
      STA
            CNT
FST
     IRS
                                O=TRACE STATEMENT NO.
      IFZ
      CALL
            F$ TR
            3,1,5, 37,8, 240 '37=HALF<, 240 FREVENTS AT SIGN
      VFD
      BCI
            2,A
      ELSE
      IMA
            F$ TR
      CALL
      VFD
            3,1
      BCI
            2,A
      IMA
            Α
      ENDC
            CNT
      IRS
            FST
      JMP
            FLAG
      LDA
            = 1
      ERA
             LOOP
      JMP
 TWO CRA
                                O=TRACE STATEMENT NO.
      1FZ
            F$TR
      CALL
             3,0,5, 37,8, 240 '37=HALF<, 240 PR VESTS AT SIGN
      VFD
             2. TWO
      BCI
      ELSE
             TWO
      IMA
             F$TR
      CALL
      VFD
             3,0
      BCI
             2, TWO
       IMA
             TWO
       ENDC
       STA
             =-2
       LDA
             CNT
       STA
 SEC
             Α
       IRS
                                 OFTRACE STATEMENT NO
       IFZ
       CALL
             F$TR
             3,1,5,137.8 1010
       VFD
       pr t
```

Figure 1-9. Typical MAC Output Code — Conditional Trace Example

1-18 #AC94

0001			*		TRACE EXAMPLE USING MACROS
0002				CF5	
0003				REL	
0004 0	0000	140040		CRA	
0005 0	0001	0 04 00053	LOOP	STA	FLAG
0011 0	0002	0 13 00053		IMA	FLAG
0012 0	0003	0 10 00000		CALI.	FRTR
0013 0	0004	060000		VFD	3.3
0014 0	0005	143314		BCI	2.FLAG
0	0006	140707			
0015 0	0007	0 13 00053		IMA	FLAG
0017 0	_	100100		SLZ	
0018 0		0 01 00031		JMP	TWO
0019 0		0 04 00055		STA	A
0020 0		0 02 00060		LDA	=-4
0021 0		0 04 00054		STA	CNT
0055 0		0 12 00055	FST	IRS	A
0028 0		0 13 00055		IMA	Α _
0059 0		0 16 00000		CALL	F S T R
0030 0		02 0000		VFD	3.1
		140640		BCI	2.A
_		120240			
0032 0		0 13 00055		IMA	A
0034 0		0 12 00054		IRS	CNT
0035 0		0 01 00015		JMÞ	FST
0036 0		0 02 00053		LDA	FLAG
0037 0		0 05 00057		ERA	=1
-		0 01 00001	-11-	JMP	LOOP
			TMO	CRA	
_		0 10 00000		CALI.	FSTR
	10000	U =		VFD	7 ^
0043 0	100-				

Figure 1-10. Typical Assembly Listing — Conditional Trace Example

1-19 #AC94

SECTION II

O16-XREF CONCORDANCE

GENERAL DESCRIPTION

The O16-XREF Concordance program prepares a cross-reference listing (concordance) of all symbols within a Honeywell DAP-16 Assembly Language source program. The symbols are listed in alphanumeric order with the defining line numbers shown to the left (along with any exception flags) and the line numbers of all references to the symbol on the right. The line (record) numbers on the left are the same as the numbers shown on the left of a source listing or an assembly listing. Figure 2-1 shows an assembly listing of a source and the corresponding cross-reference. The source was written to show the features of O16-XREF.

Symbol recognition is less restrictive than the similar process in the assemblers for the same language. Symbols which are almost acceptable are detected and the programmer is made aware of them.

The four exception flags are:

M — Illegal multiple definition of symbol

N - Symbol defined but never referenced

S - Legal multiple definition of symbol

U - Undefined symbol

If lags M and U indicate lines of a finished program which are usually clearly in error. The N flag indicates a symbol which is not required by the assembler and may point out a programmer oversight. An acceptable multiple definition is indicated by an S flag.

Each reference to a symbol can have a C or J suffix to the line number to indicate a change or jump, respectively. The C indicates that the referenced instruction is a STA, DST, STX, IMA, or IRS. A J results from a JMP or JST instruction. It should be noted that the C and J suffixes are useful only when the nature of the code is known, since the actual instruction and referenced address are subject to indexing, indirect addressing, program self-modification, etc.

2-1 #AC94

```
XREF EXAMPLE
   0001
   0002
                                                            NO SYMBOLS NORMALLY HERE
                                  REL
                                         ABCD
                             ABC
   0003
                                  EXT
                                         BCD
                                                            EXTERNAL DEFINITION
   0004
                                                            NO SUFFIX
                                         BCD
   0005 00000
                 0 02 00000
                                  LDA
                                                            C SUFFIX
                                  STA
                                         BCD
                 0 04 00000
   0006 00001
                                   JMP
                                         BCD
                                                            J SUFFIX
   0007 00002
                 0 01 00000
    8000
                                                            THREE LITERALS WHICH HAVE
                                         =16
   0009 00003
                 0 02 00016
                                  LDA
                                                            THE SAME LITERAL VALUE, BUT
                                         = '20
                 0 02 00016
                                  LDA
    0010 00004
    0011 00005
                  0 02 00016
                                  LDA
                                         =$10
                                                            A DIFFERENT SYMBOLIC VALUE
                                                            C SUFFIX FOR A LITERAL
                  0 13 00017
                                   IMA
                                         = A X Y
    0012 00006
    0013
                                                            ONE M FLAG FROM JKL
                  0 000016
                             JKL
                                  DAC
                                         =16
    0014 00007
                                                            TWO NORMAL REFERENCES
                                  STA
   0015 00010
                  0 04 00007
                                         JKL
                  0 02 00007
                                  LDA
                                         JKL
    0016 00011
                                                            BOTH M AND N FLAGS FROM JKL
                             JKL
                                  BSZ
                                         3
                 000000
    0017 00012
    0018
                                                            U FLAG ON MNO
    0019 00015
                  0 04 00024
                                   STA
                                         MNO
                                                            NO SYMBOLS NORMALLY HERE
    0020 00016
                  000020
                                  FIN
                                         MNO
         00017
                 154331
    0021
                                                            NO POOLING SHOWN
                  0 02 00023
                                  LDA
                                         =16
    0022 00020
                                                            POR GETS N FLAG
                                         =16
    0023 00021
                  0 10 00023 PQR
                                   JST
    0024
                             RETA SET
                                                            S FLAG ON RETA
                  000022
    0025
                                                            ILLEGAL SYMBOL
                  0 000000
                             183 XAC
                                         OSLA
    0026 00022
                                                            S FLAG
                             RETA SET
                                         JKL+ABC
MV 0027
                  000007
    0028
                                   END
                                         JKL THIS IS A COMMENT
    0029 00023
                  000020
RETA
         00024
MNO
         00025
                                                                  000021
                                                000025
                                                          POR
                             000007
                                        MNO
    BCD
           000000E
                      JKL
    RETA
           000024
    0009 WARNING OR ERROR FLAGS
                               10-20-70
    DAP-16 MOD 2
                     REV. B
```

Figure 2-1. XREF Example (Sheet 1 of 2)

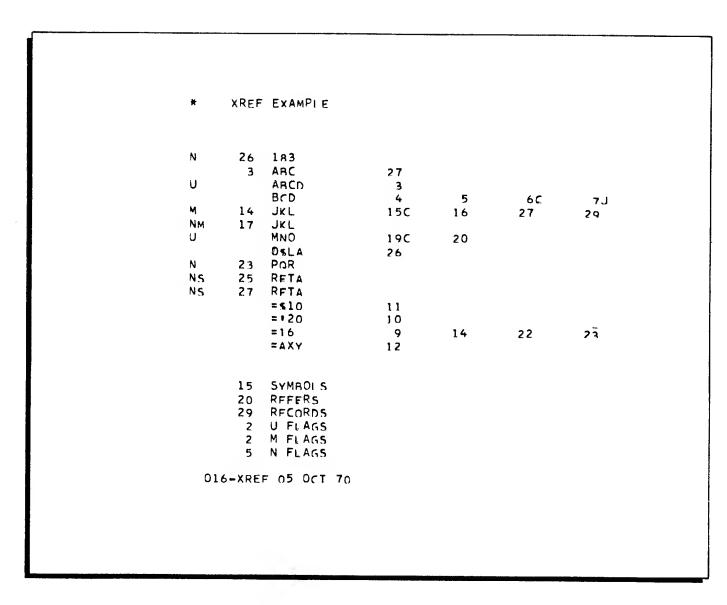


Figure 2-1 (cont). XREF Example (Sheet 2 of 2)

Literals are treated as self-defining symbols rather than being evaluated. Therefore, the terms = '20 and = 16, which the assembler may evaluate, are listed separately. The FIN pseudo-operation used to pool the literals is ignored.

SYSTEM GENERATION

The generation of an XREF system is performed in a straightforward fashion. The ORGed object of O16-XREF is loaded and this calls for the off-line I/O driver programs directly, with the exception of the Disk/Drum Operating Program (DOP) and its driver. Drivers for the desired devices, including the magnetic tape support programs, can be loaded in any order. Calls for unwanted device drivers can be satisfied with DUMY-X16.

If the Honeywell-supplied magnetic tape support package is used, the channel type and number (if DMC or DMA is to be used) must be entered. The Ol6-XREF program uses logical unit 1 for input (same as the assembler) and this may also require an entry. Complete information is contained in the appropriate option Programmers' Reference Manual.

When DOP is used, the linkages can be placed in sector zero either by directly entering them or by starting DOP.

The load map should be examined for a high greater than '5000. If this is the case, the contents of location GRMA ('464 in Rev. B) must be changed to the next higher address ending in octal zero. This address indicates the lowest memory usable for storing cross-reference data. Conversely, if the high address is much below '5000, the contents of GRMA can be changed to a lower address.

Locations '1000 and '1001 contain NOPs. The second location can be used for a LDA SYSP with the installation preferred keyword placed in SYSP ('506 in Rev. B) if desired.

OPERATION (Keyword)

The source to be cross-referenced must be made ready on the input device prior to starting O16-XREF. If magnetic tape is the input device (logical unit 1), it must be at the first record of the desired file. The input process is normally terminated upon detection of either an END pseudo-operation record or an end-of-file mark. If the input was from magnetic tape, the tape will be rewound at this time.

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The installation-preferred keyword must be set into the A register prior to the start. Figure 2-2 is presented as a guide to keyword selection. If any field is left blank, the indicated default value for that field is assumed.

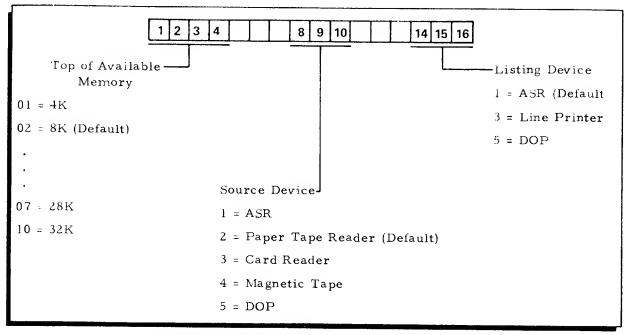


Figure 2-2. Setting of A Register for Keyword

Start the program at (P) = '1000. If the keyword is not acceptable, the message PARM ERROR is printed on the ASR and the processor halts with the keyword in the A register. The keyword must be changed and the processor restarted from either the halt address or location '1000.

At the end of the process, the processor halts and displays the keyword in the A register. Another concordance can be generated using the same or a different keyword. The concordance is listed as soon as a single input pass is complete.

MEMORY REQUIREMENTS AND OVERFLOW

On the average, a symbol is defined every five lines of code and each symbol is used five times. Each symbol requires eight words and a reference requires two words. Based on a GRMA of '5000, the source programs of various size listed in Table 2-1 can usually be cross-referenced in one operation.

If the available memory is filled before the input is complete, the cross-reference to that point is listed with flags suppressed. After the operation is complete, the message SEGMENT BOUNDARY and the cumulative counts are printed. Then, in effect, a program

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restart is made which picks up the source input at the point where it left off. The final section is terminated with the message FLAGS OMITTED.

Table 2-1. XREF Memory vs Program Size

Memory	Records
4K	420
8K	1560
12K	2700
16K	3800
20K	5000
24K	6100
28K	7200
32 K	8400

TERMINATION

The concordance is terminated with the record, symbol, and reference counts along with the count of M, N, and U flags. If a segmented concordance was generated, the cumulative counts of records, symbols, and references are listed along with the message FLAG OMITTED.

MISCELLANEOUS CONSIDERATIONS

The first record encountered is taken as the title line for all pages. It is also processed as a normal record. The presence of either unexpanded macro instructions or conditional assembly statements can produce warning flags which must be interpreted by the programmer.

External names are not indicated as such, but in general they may be noted by a blank-defining record field. The use of code such as

A\$BC XAC A\$BC

will obscure the external indication. References to multiply defined symbols are taken as referring to the first definition encountered. Subsequent definitions are listed, but they receive an N flag as well as an M flag.

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SECTION III SSUP SYMBOLIC SOURCE UPDATE

DESCRIPTION

SSUP and its associated Input/Output Supervisor, SSUP-IOS, form a file maintenance system for Honeywell DAP-16 Assembly Language and FORTRAN source files. Figure 3-I presents an overview of program flow. The old master or starting source is processed against the update stream, which contains both commands and new records to be inserted. The result of the processing is either a new master, sometimes called an updated source, or both the new master and a listing.

COMMAND LANGUAGE

The update process is controlled by commands which appear in the update stream. The format of the commands is presented in Figure 3-2.

The elementary items within the square brackets may be optional. Spaces can be used freely to separate the elements, except that a blank must follow the single letter commands N and O.

The SSUP commands consist of one or four letters. The commands and their functions are presented in Table 3-1.

An F in the file/record indicator indicates that the command is to be applied to files rather than records. This mode is usually applicable to magnetic tape. When the file/record indicator is blank, the command is applied to a record.

The limit arguments, which specify record or file numbers, are decimal integers not greater than 32,767. The meaning of these arguments is described in the examples which follow. Either or both of these arguments can be absent from all commands except NSRT, OMIT, BEGN, and COPY. These four commands require at least one argument.

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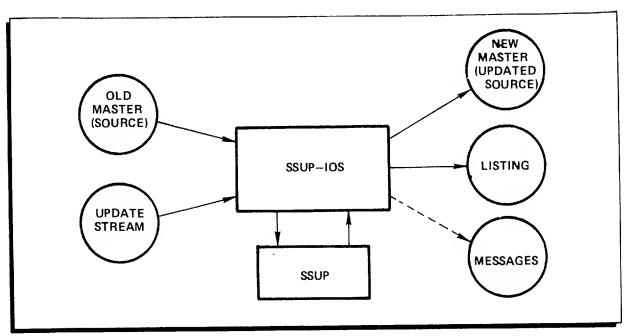


Figure 3-1. SSUP Program Flow

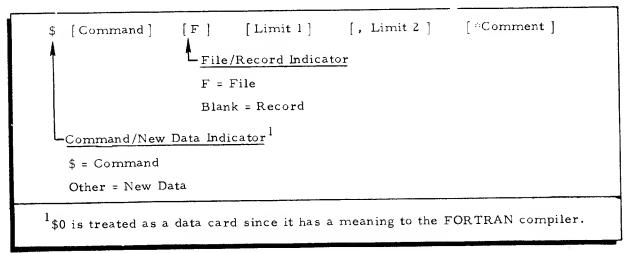


Figure 3-2. SSUP Command Format

An asterisk anywhere in the command causes the remainder of the line to be treated as a comment. Hence, the remainder of the line is ignored for processing, but it is listed if commands are being listed.

A command which contains only a comment is acceptable.

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Table 3-1. SSUP Commands

Command	Function	
NSRT or N	Insert new records.	
OMIT or O	Omit old records or omit old records and insert new records.	
LIST	List all records that come from old master plus all corrections that come from update source.	
NLST	No records are listed. SSUP is initially in NLST mode.	
BEGN	Position new master (used with magnetic tape only).	
COPY	Copy old master on new master.	
NCPY	Not generally useful: turns copy indicator off and nothing is written on new master.	
HALT	Stop all I/O operations. Wait for operator action.	
RSET	Reset input counters to record zero, file one (useful for concatenating old sources onto one new source).	
WEOF	Place end-of-file mark in new master.	
DONE	Place end-of-file mark in new master and terminate SSUP process.	
DOLR	Change command indicator (originally \$) to next nonblank character. Useful for updating master files.	

SUMMARY EXAMPLE

The example in Figure 3-3 is a typical update stream which uses the more common commands and functions. It is correct as shown for the old and new masters on magnetic tape and the update stream on cards. The flow is generally correct regardless of the media.

In general, there are several combinations of commands and limits which will accomplish any task. The example presented in Figure 3-3, with reasonable extensions by the reader, is a straightforward approach which can accomplish all ordinary update tasks.

Operation	Command	Functions	Comments
Position Output Tape	\$BEGNF m	Advance output (new master) tape past m-l file marks.	Tape is now in position to write n-th record on m-th file of output tape.
	\$BEGN n	Advance output tape past n-1 records,	Tife of output tape.
Position Input Tape	\$OF, p	Skip files from current position to beyond file p.	New tape contains some number (between none and all) of files from old master.
	\$COPYF, q	Copy from current position through and including file q.	
Perform Update	\$ N r	Copy old master from current position up to and including (r-1)th record. Insert following records until the next command is found.	
	(new record)		
	(new record)	C 11 a satau fuens	
	\$ O t	Copy old master from current position up to and including (t-1)th record. Insert following records, if any.	
	[(new record)		
	\$ O u, v	Copy old master from current position up to and including (u-1)th record. Skip records u through v. Insert following records, if any.	
	[(new record)		
	(new record)		
Copy .* Remainder of Input Tape	\$COPYF, w	Copy old master from current position (record v+1) throught in the conditions of the	
Terminate the Process ¹	\$ WEOF	Write end-of-file mark.	
	\$ DONE	Write end-of-file mark and indicate job is complete.	

Two successive end-of-file marks (i.e., a blank file) customarily indicate the end of data on a magnetic tape.

Figure 3-3. SSUP Summary Example

LISTING

Two types of listings, plus a no-listing option, can be obtained from SSUP. One listing is a transaction listing which is the default condition. All commands and comments are listed, along with the results of omit and insert actions. The records inserted are shown with a plus sign and the record number they acquired in the new master. The records omitted are shown with a minus sign and the record number they had in the old master. The transaction listing may be suppressed either by setting sense switch 4 or by including the LIST command.

The LIST command overrides the transaction listing and causes the second type of listing to be generated. In this type, a line is output for every record read from the old master or the update stream. If there is no update, the resultant list is a source listing with the decimal record numbers printed on the left side. If no limits are stated, all records will be listed. If only [, limit 2] is stated, all records from the current position through record "limit 2" will be listed.

NSLT turns the LIST option off and restarts the transaction listing.

Listings produced on the teletype are truncated after card image column 72. The tab format source is expanded to the conventional columns before being listed (i.e., tabbed).

MESSAGES

There are two SSUP error messages, NONVALID COMMAND and LIMIT ERROR. An error message is followed by the current input (usually a command), the current file, and record counts. A reminder is also printed that informs the operator he may override the error or terminate the process. The override is accomplished by pushing START with sense switch 3 reset. The update stream will be ignored until the next command record is detected. If sense switch 3 is set, the effect is the same as a DONE command. These error messages are shown in the examples.

There is one I/O error message possible if the old master is being read from magnetic tape. A nonrecoverable read error will cause the message PARITY ERROR and the file and record counts to be printed. Following the printout, the computer halts. If the A register is cleared, another read will be attempted when START is pushed. If the A register is not cleared, the record will be accepted as read. Sense switch 2 will suppress the error message when it is set, but not the halt (with all bits set in the A register).

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Unexpected end-of-file marks or end-of-tape being sensed will cause a halt without an error message.

Other messages are BREAK POINT HALT and END OF JOB. The BREAK POINT HALT is output when a HALT command was encountered, presumably for the operator to change the input stream.

RESEQUENCE OPTION (Keyword)

If the output is magnetic tape, SSUP can resequence card image columns 77 through 80 by either 1's or 10's depending upon the keyword in the A register at the start. If resequencing is selected, the ID field (columns 73 through 76) is taken from the first record to be written on the new master. The A register keywords for the various options are:

'000000 - Resequence by 1's

'100000 - Resequence by 10's

'040000 - Copy input verbatim

SENSE SWITCH OPTIONS

The functions of the sense switches are presented in Table 3-2.

Switch Condition Function 1 Reset DAP-16 format Set FORTRAN format 2 Reset Normal Set Suppress parity error message 3 Reset Continue process on error Set Terminate process on error restart 4 Reset Normal Set Suppress transaction listing

Table 3-2. SSUP Functions of Sense Switches

SOURCE FORMAT

If paper tape or the Teletype is used, the format must be either the correct tab form for the specific language or in acceptable columns. FORTRAN tab settings are at columns 6, 7, and 73. DAP-16 tabs are at columns 6, 12, and 30. Lines are opened with a line feed and closed with a carriage return.

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Magnetic tape must have 80-character records.

OPERATION

The operator must load an SSUP system configured for the desired devices, since SSUP cannot be configured at run time. If magnetic tape is to be used, the old master must be mounted on logical unit 1 and the new master on logical unit 2. This is consistent with the assembler and compiler assignments.

The A register must be set with the keyword, and the computer started at location '1000. Successive jobs must have the keyword restored and started at location '1000.

If the output is being punched on paper tape, the operator must manually punch leading and trailing blanks and turn off the punch at the end of the job.

SYSTEM GENERATION

An SSUP system must be generated for each combination of I/O devices desired. The allowed combinations and required device drivers are:

Old Master	-	Magnetic Tape (logical unit 1) Paper Tape Reader	I\$MA I\$PA
New Master	-	Magnetic Tape (logical unit 2) Paper Tape Punch	O\$MA O\$PA
Update	- - -	Card Reader Teletype Paper Tape Reader	I\$CA I\$AA I\$PA
Listing	-	Teletype Line Printer	O\$LL O\$LA
Messages	-	Teletype	O\$LL

The use of the Teletype as both the update master and the listing device is acceptable. The order of loading must not conflict with the above if the desired device stream attachments are to be made.

If other combinations of I/O devices are desired it is possible to attach them by writing a driver which appears to SSUP-IOS as an allowable device. This is not supported by Honeywell.

SSUP will operate in a 4K memory computer, but at least 8K is required to generate a system.

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Load the ORGed SSUP and SSUP-IOS. Then select and load (consistently with the previously noted order) the desired device driver and support programs for the four streams. If the Teletype I/O library which includes O\$LL was not loaded, do so at this point for the message output. Progress should be checked at this point by generating a memory map and observing that the correct calls are satisfied. Satisfy the remaining calls by loading SSUP-RDS, the associated dummy routine set.

If magnetic tape is to be used, the type of I/O channel (DMA or DMC) and the channel number along with the logical-dialed number relationship must be entered. Refer to the Programmers' Reference Manual of the appropriate option for further instructions.

EXAMPLE OF PAPER TAPE UPDATE

The following file was punched with a Teletype to demonstrate the use of SSUP. It anticipates DAP format (i.e., tab placement). All records start with a line feed and close with a carriage return. The use of an X-OFF and rubout is not required, since it will only be read by a paper tape reader. A nonprinting record following the printing text consists of a line feed, ETX, and carriage return. The ETX, which is punched by simultaneous CTRL and C keys, is recognized as an end-of-file.

ORG\REC\ONE
ORG\REC\TWO
ORG\REC\THREE
ORG\REC\FOUR
ORG\REC\FIVE

The first step in most source updates is to obtain a list of the file with record numbers. In this case a copy of the file after corrections that were made during the original punching is also obtained. Note that the Teletype shows an echo of the commands.

SLIST

SLIST

\$COPYF1

SCUPYFI

00001 ORG REC ONE REC TWO 00002 ORG THREE 00003 ORG REC 00004 ORG REC FOUR 00005 ORG REC FIVE **SDONE**

SDONE

END OF JOB

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The programmer decides that records 2 and 3 are to be replaced and a new record is to be inserted between records 4 and 5. He elects to list the transactions by leaving sense switch 4 reset.

\$ 0 0002,3 * FREE FORM - "O" AND "OMIT" ARE SYNONYMS

\$ 0 0002,3 * FREE FORM - "O" AND "OMIT" ARE SYNONYMS

-00002 ORG REC TWO
-00003 ORG REC THREE
NEW RECORD AAA
+00002 NEW RECORD AAA
\$DOLR . * CHANGE COMMAND INDICATOR

\$DOLR . * CHANGE COMMAND INDICATOR

.N 5

.N 5

\$NEW RECORD BBB HAS A "\$" AS THE FIRST CHARACTER +00004 \$NEW RECORD BBB HAS A "\$" AS THE FIRST CHARACTER .COPYFI

.COPYFI

.DONE

.DONE

END OF JOB

Since the new record starts with a control character, the command indicator in use was changed from a dollar sign to a period.

As a final step the programmer listed the results. He chose not to copy the tape and because of this the listing shows each record as being deleted. In typing the commands, two errors were made to show the error messages.

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```
SLIST
```

SLIST

SOMIRF!

NON-VALID COMMAND

SOMIRF!

FILE 00001 RECORD 00001

SSW3 SET TO TERMINATE, RESET TO PROCESS NEXT COMMAND DEPRESS START.

\$OMITF2.!

SOMITF2,1

LIMIT ERROR.

SOMITF2.1

FILE 00001 RECORD 00001

SSW3 SET TO TERMINATE, RESET TO PROCESS NEXT COMMAND DEPRESS START.

SOMITF!

SOMITF!

-00001 ORG REC ONE -00002 NEW RECORD AAA

-00003 ORG REC FOUR

-00004 SNEW RECORD BBB HAS A "\$" AS THE FIRST CHARACTER

-00005 ORG REC FIVE

SNCPY

SNCPY

\$DONE

SDONE

END OF JOB

EXAMPLE OF MAGNETIC TAPE UPDATE

The first step to this example was to create three files by inserting records before the first record of a non-existent file. The cards used are shown as Figure 3-4.

The programmer decided to update his source files by eliminating the first, changing the second, and keeping the third intact. The original (or old master) remains intact. The new master contains entirely new data.

Figures 3-5 and 3-6 are the transaction listing and a listing of the new master.

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#A C94

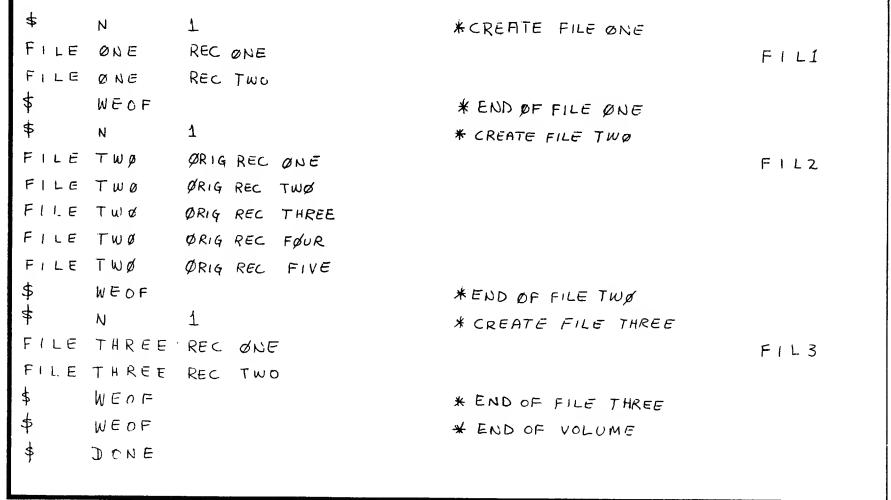


Figure 3-4. SSUP Example of Source for Magnetic Tape Update

	\$	LIST		
	\$	OMITE	* SKIP A FILE	
-00001 -00002 -00003			REC ONE REC TWO	FIL10001 FIL10001, FIL10001
	\$	0	2,3	
00001 -00002 -00003 +00002	FILE	TWO TWO	ORIG REC ONE ORIG REC TWO ORIG REC THREE AAA	FIL20001 FIL20002 FIL20002 FIL20002
	\$	DOLR	•	
	•	N	5	
00003 +00004	FILE SNEW	TWO RECOR	ORIG REC FOUR D BBB STARTS WITH A DOLLAR SIGN	FIL20003 FIL20004
	•	CUPYF	* REST OF TWO AND THREE	
00005	FILE	TWO END	ORIG REC FIVE	FIL20005 FIL20006
00001	FILE	THREE	REC ONE REC TWO	FIL30001 FIL30002 FIL30003
	•	WEOF	* END OF VOLUME	
	•	DUNE		

Figure 3-5. SSUP Example of Full Listing During Update

FILE TWO ORIG RÉC ONF	FIL20001
FILE TWO ORIG REC ONE NEW RECORD AAA FILE TWO ORIG REC FOUR SNEW RECORD BUB STARTS WITH A DOLLAR SIGN FILE TWO ORIG REC FIVE END	FIL20001 FIL20002 FIL20003 FIL20004 FIL20005 FIL20006
FILE THREE REC OME	F[L30001
FILE THREE REC OME FILE THREE REC TWO END	FIL30001 FIL30002 FIL30003

Figure 3-6. SSUP Example of New Master

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AC94

TITLE	SERIES 16 016-XREF, SSUP, AND MAC SOURCE LANGUAGE PROCESSORS	ORDER No.: AC 94, REV. 0
ERROF	RS IN PUBLICATION:	DATED: JULY 1971
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